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APPLICANTS

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** CONTINUING DATA None PL

** FOREIGN APPLICATIONS Verify PL

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| 35 USC 119 (a-d) conditions met | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance | | | | |
| Verified and Acknowledged | <u>PL</u> | <u>PL</u> Initials | DRAWING 9 | CLAIMS 7 | CLAIMS 5 |

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TITLE

Data output circuits for synchronous integrated circuit memory devices

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